

Fig. 1

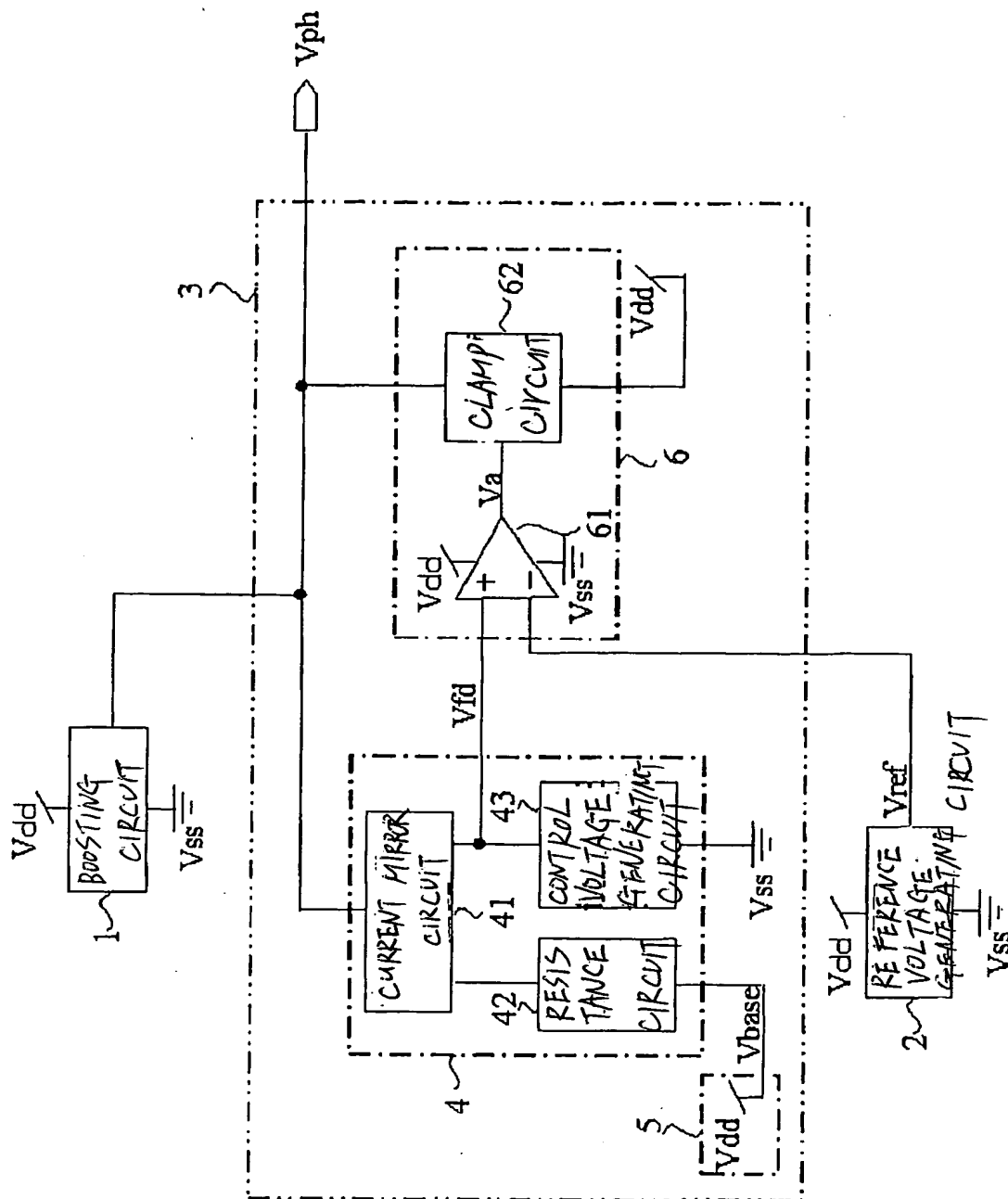






Fig. 6

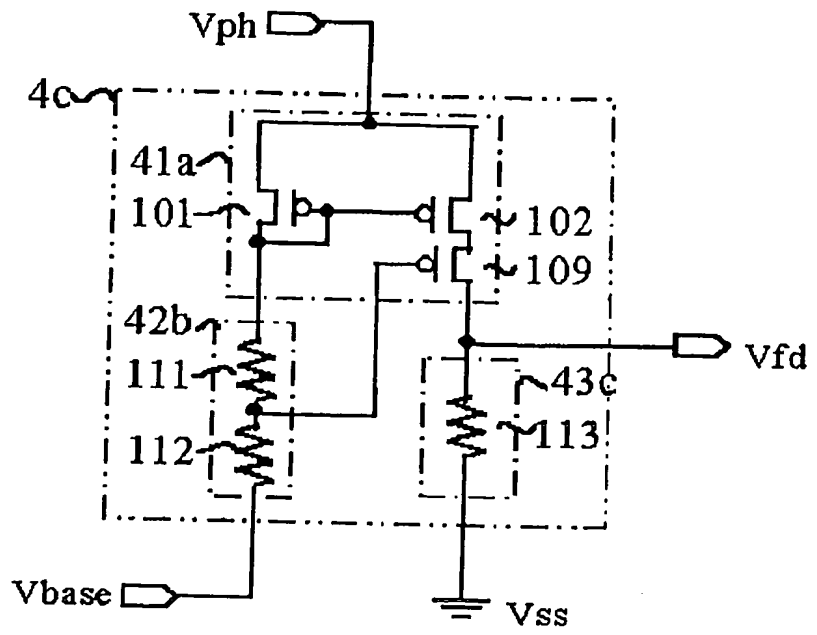


Fig. 7

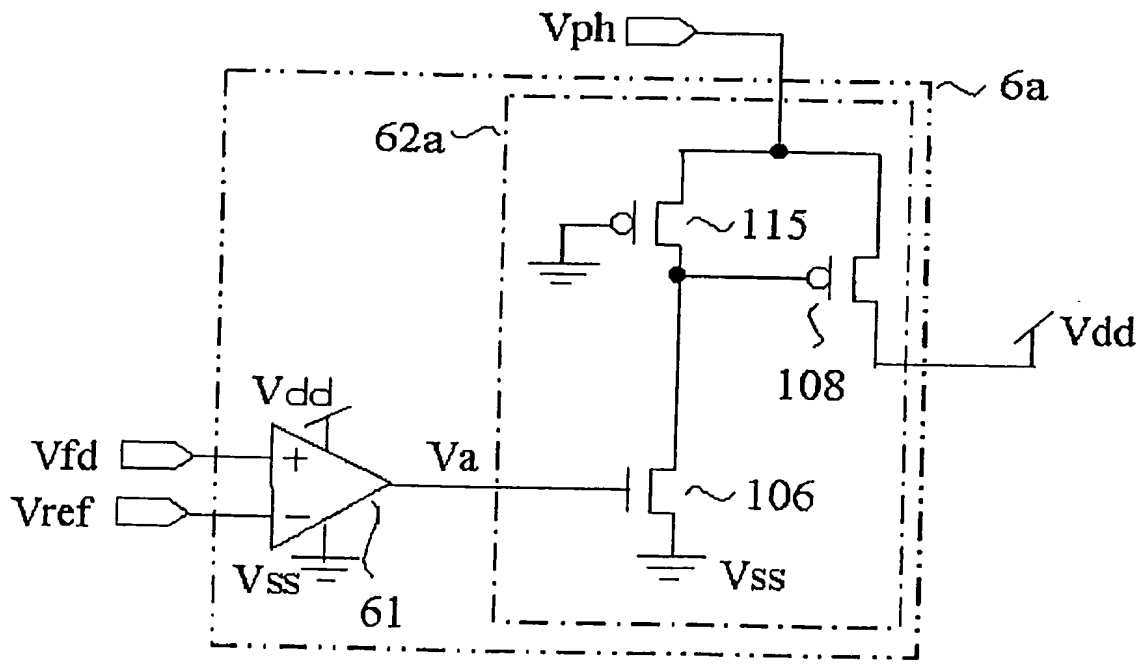


Fig. 8

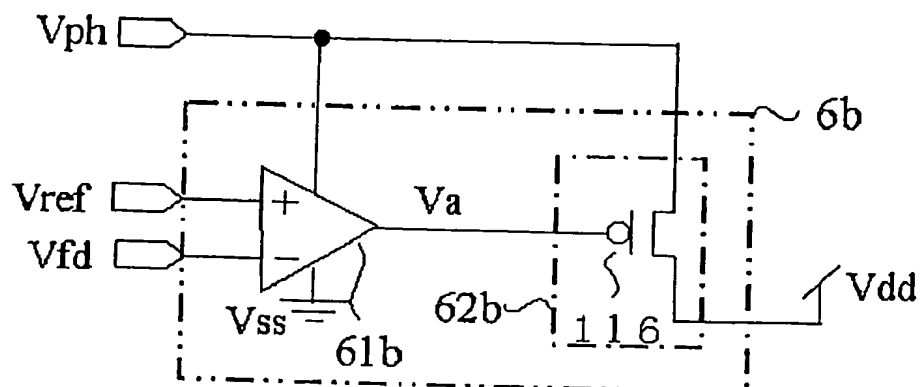


Fig. 9

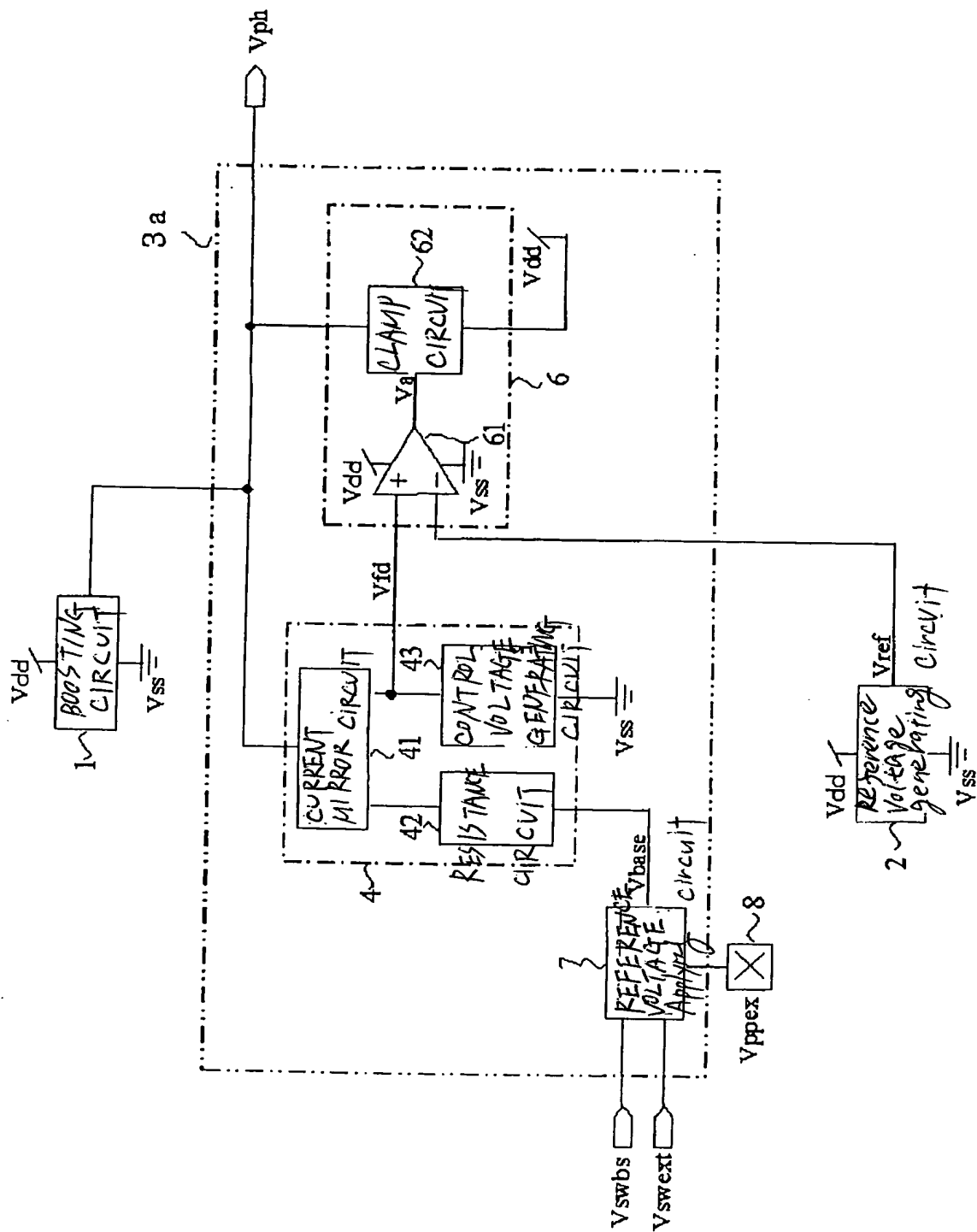


Fig. 10

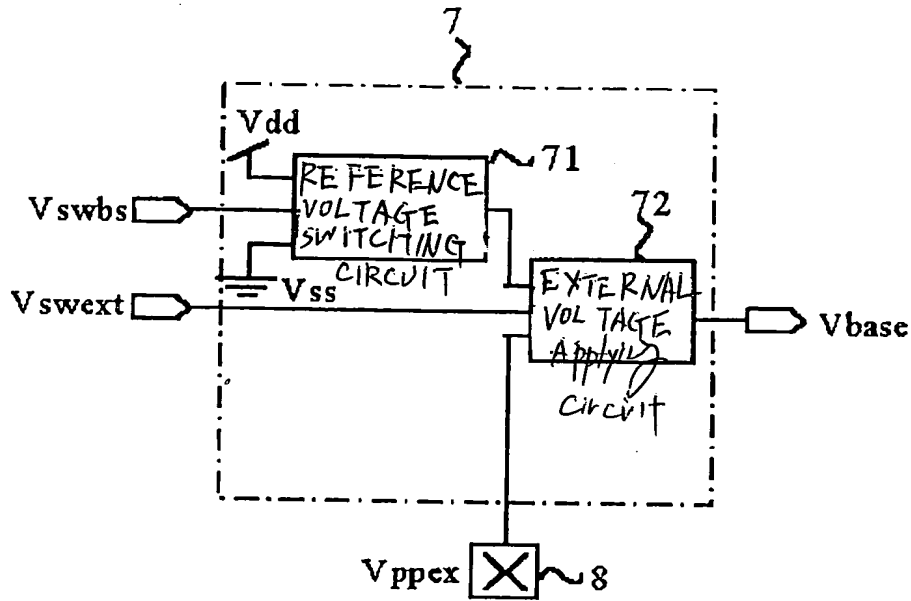


Fig. 11

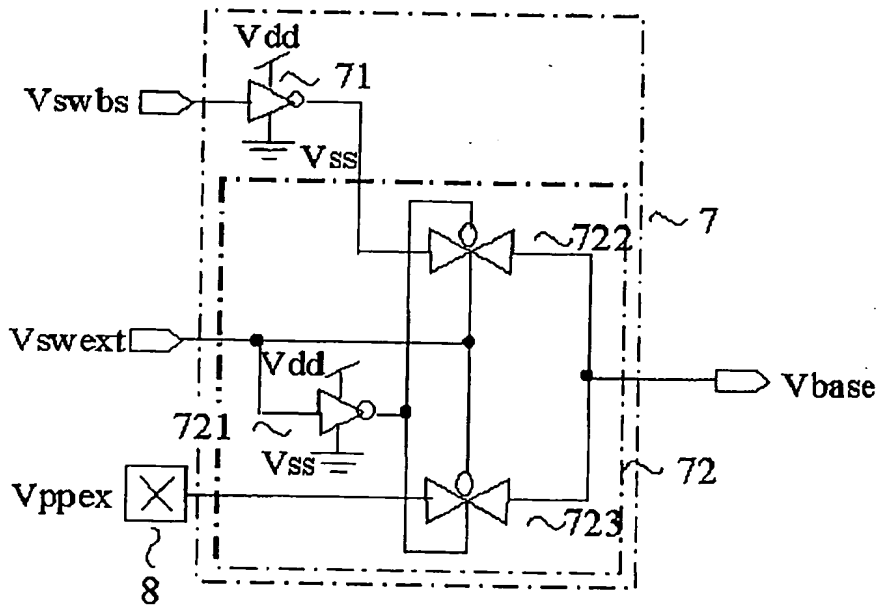


Fig. 12

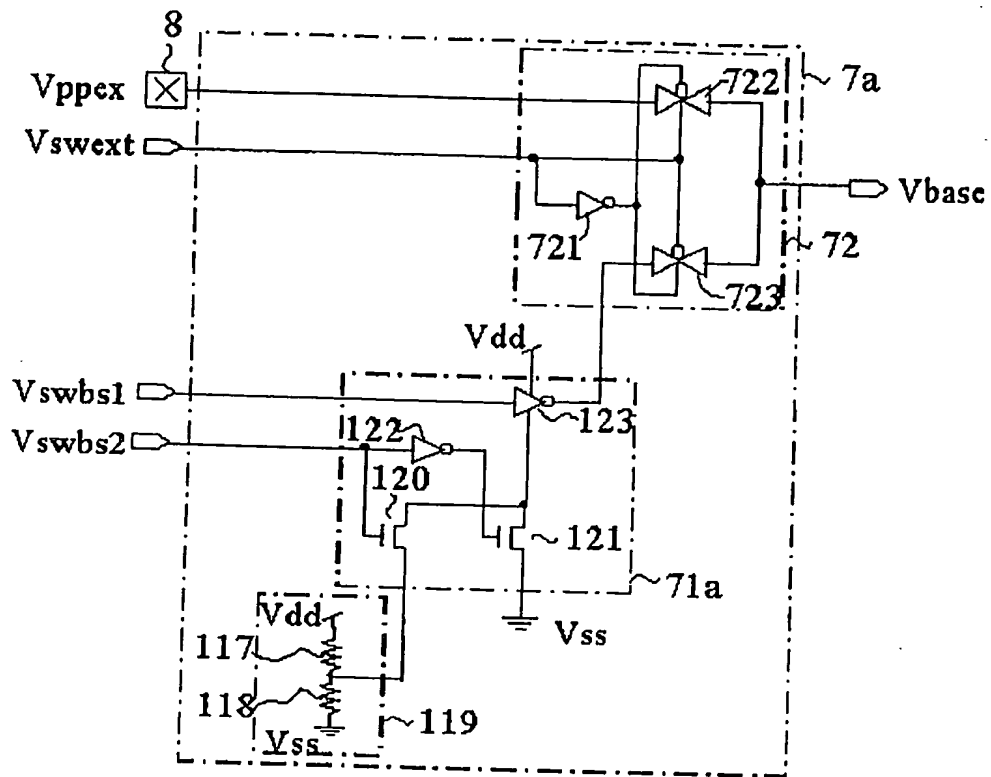




Fig. 13

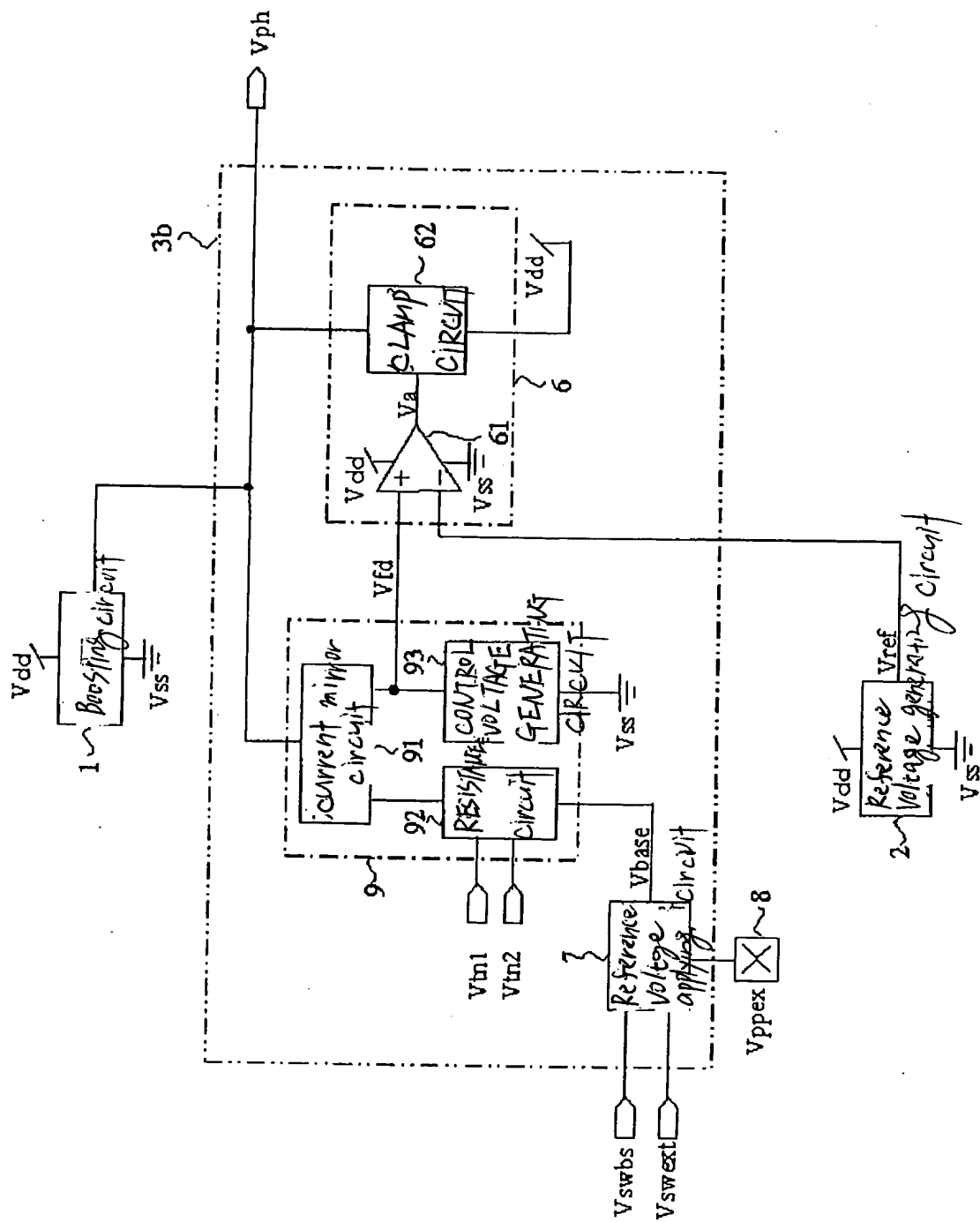




Fig. 16

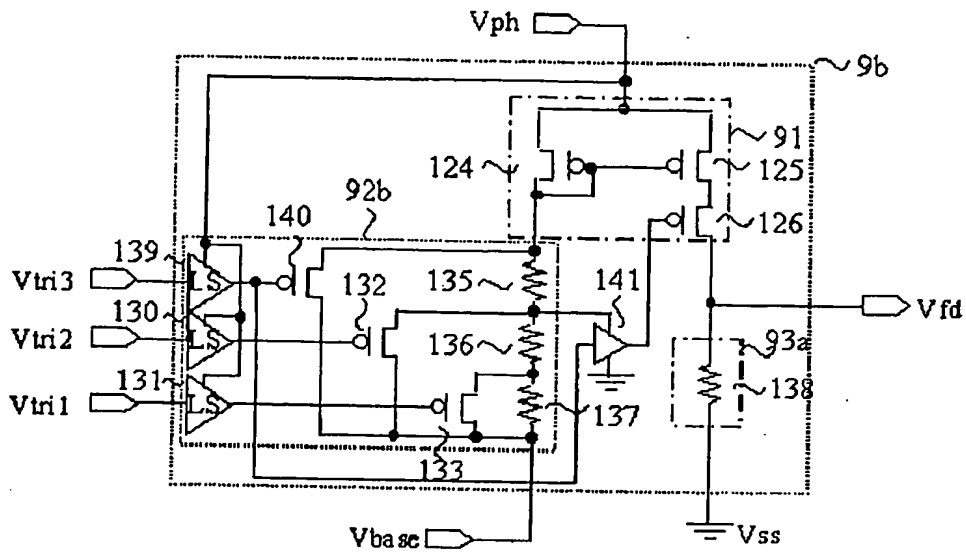


Fig. 17

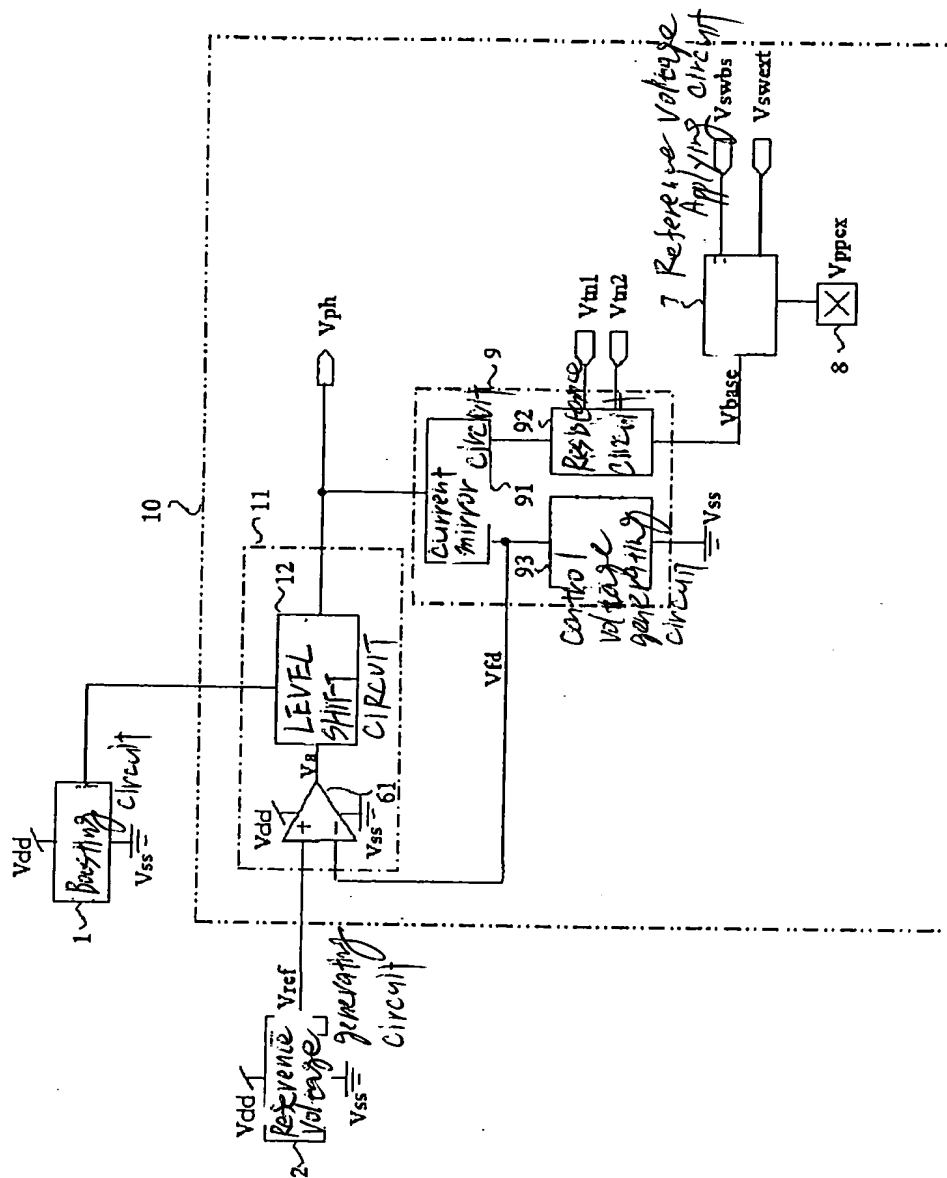


Fig. 18

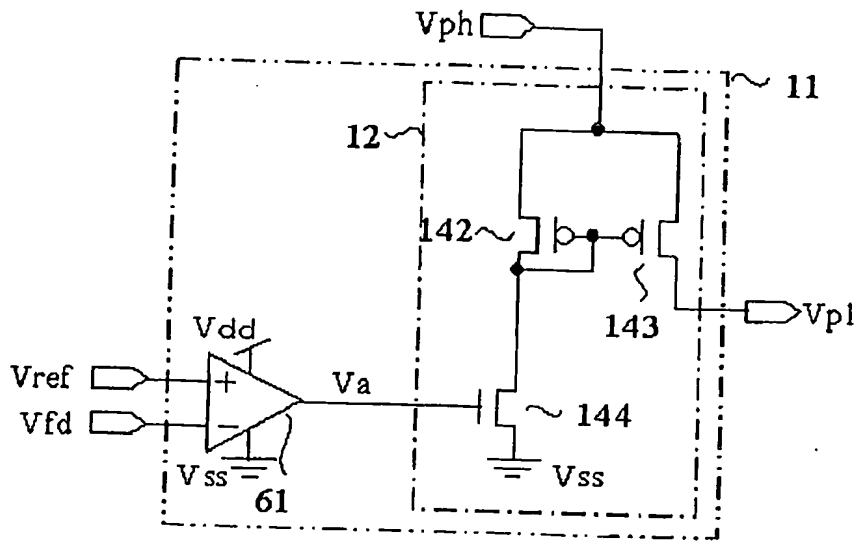


Fig. 19

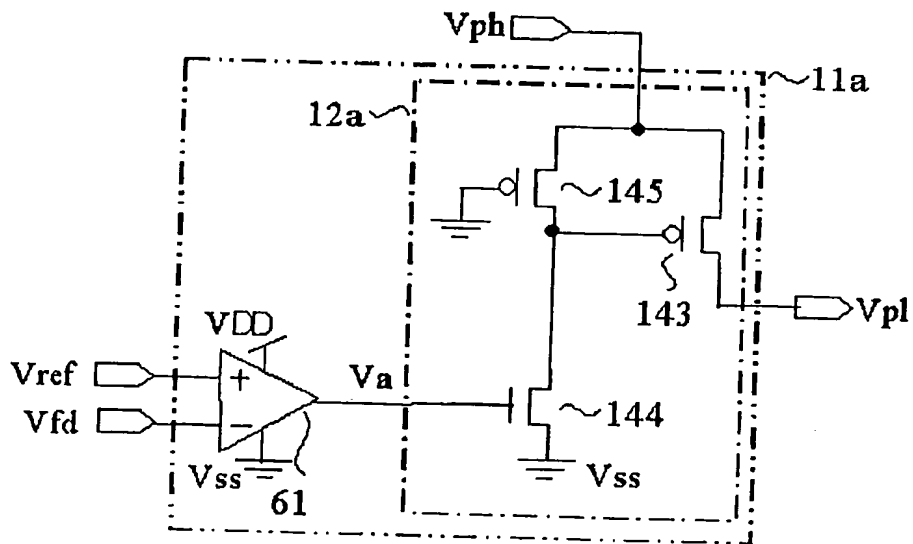


Fig. 20

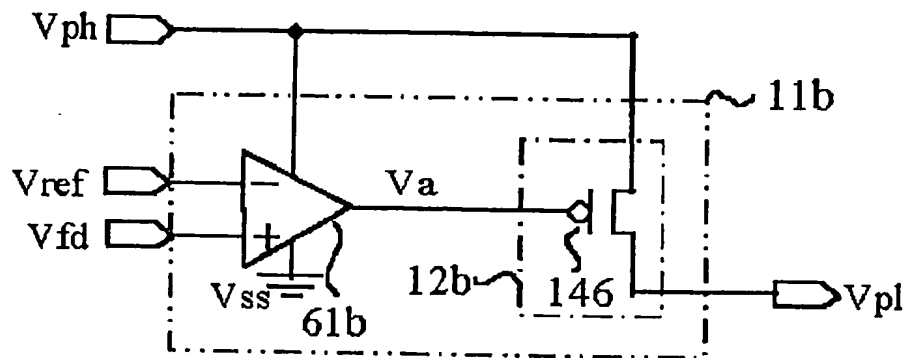


Fig. 21

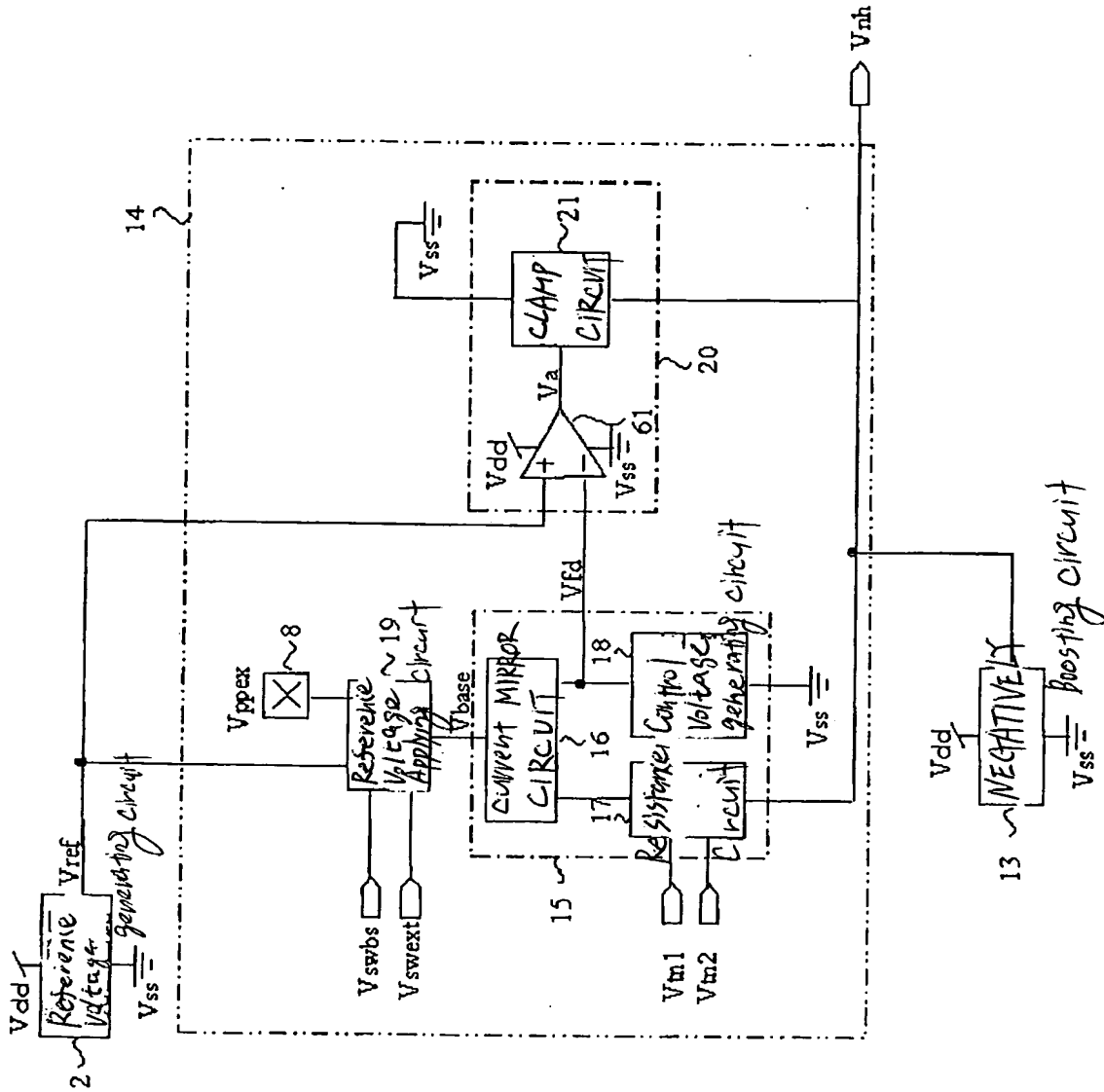


Fig. 22

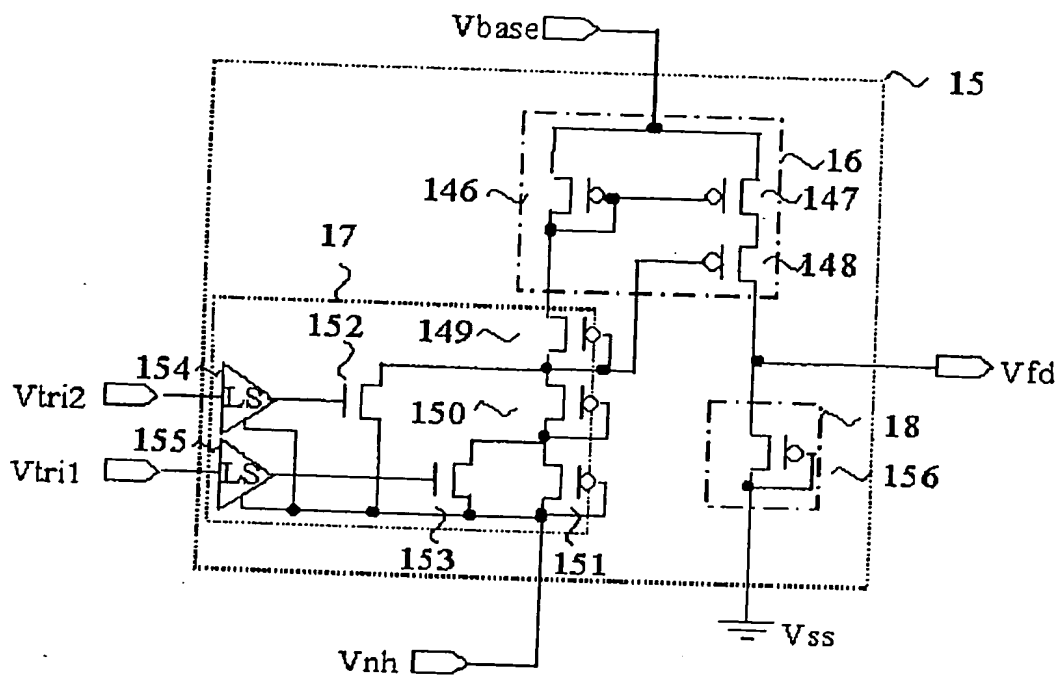


Fig. 23

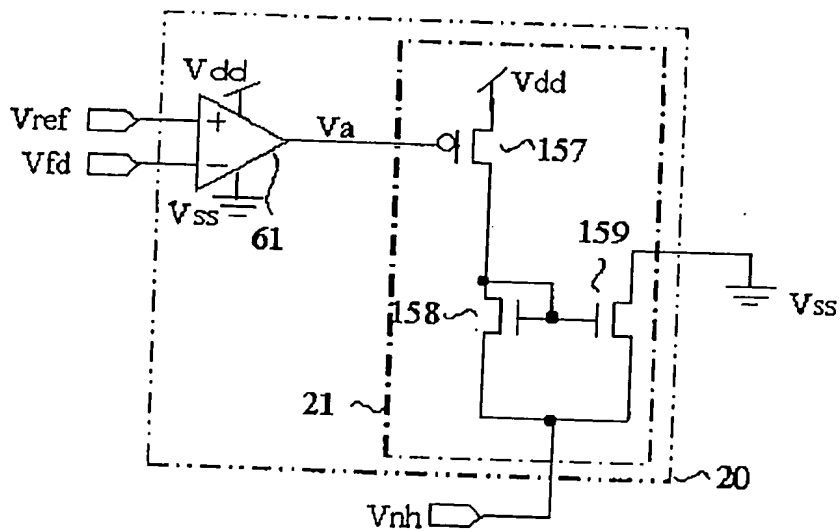




Fig. 24

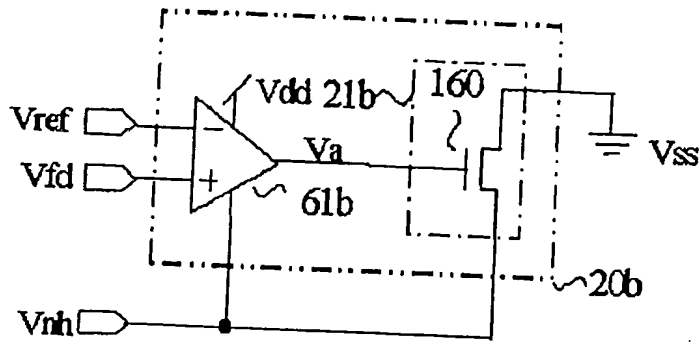


Fig. 25

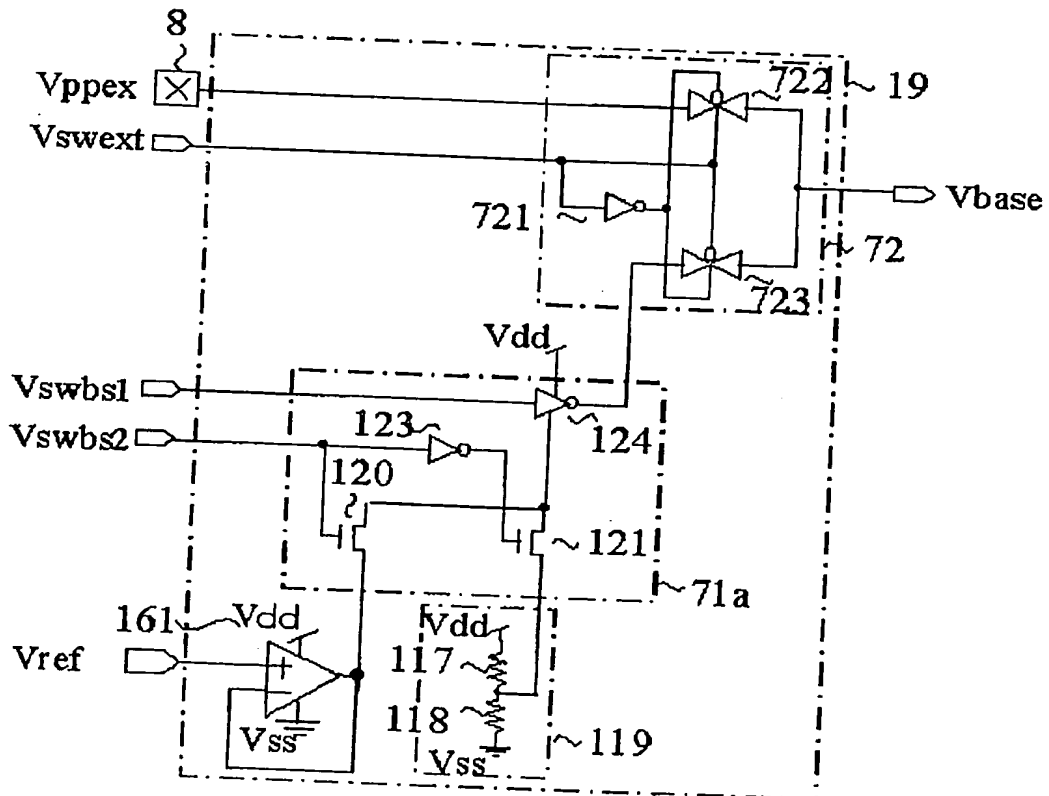


Fig. 26

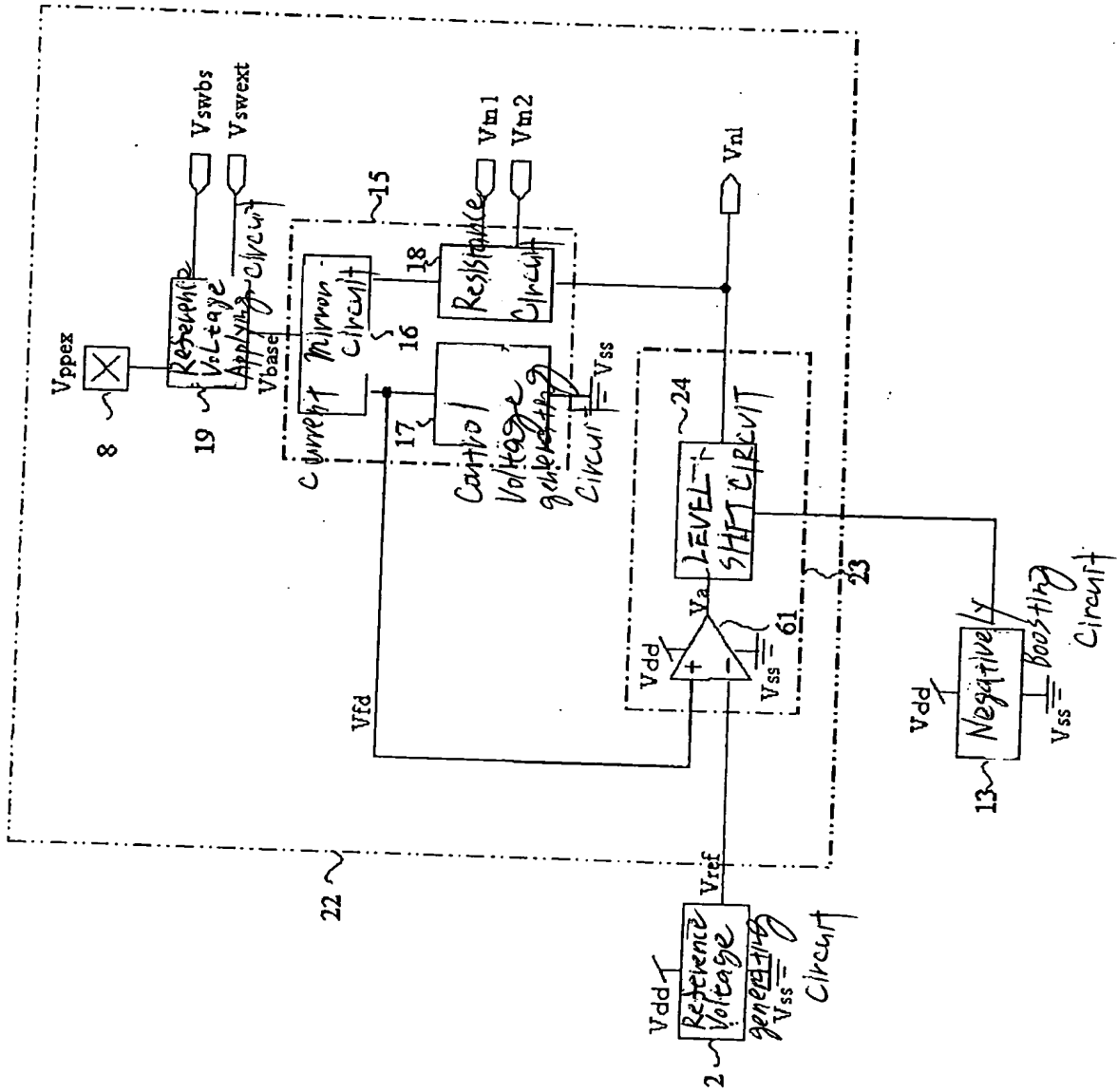


Fig. 27

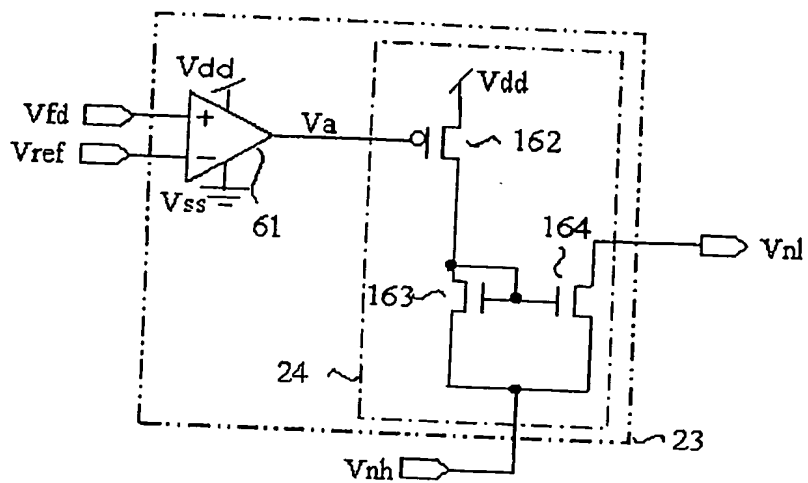


Fig. 28

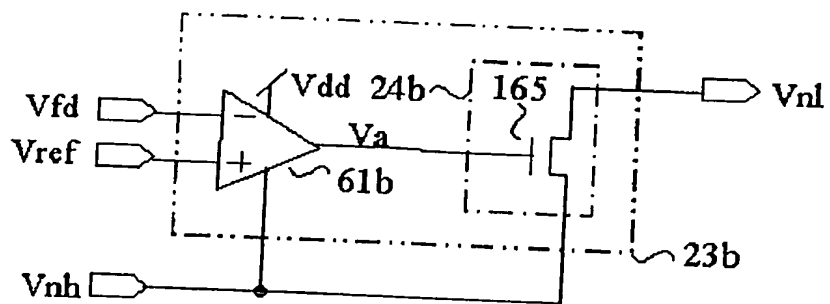
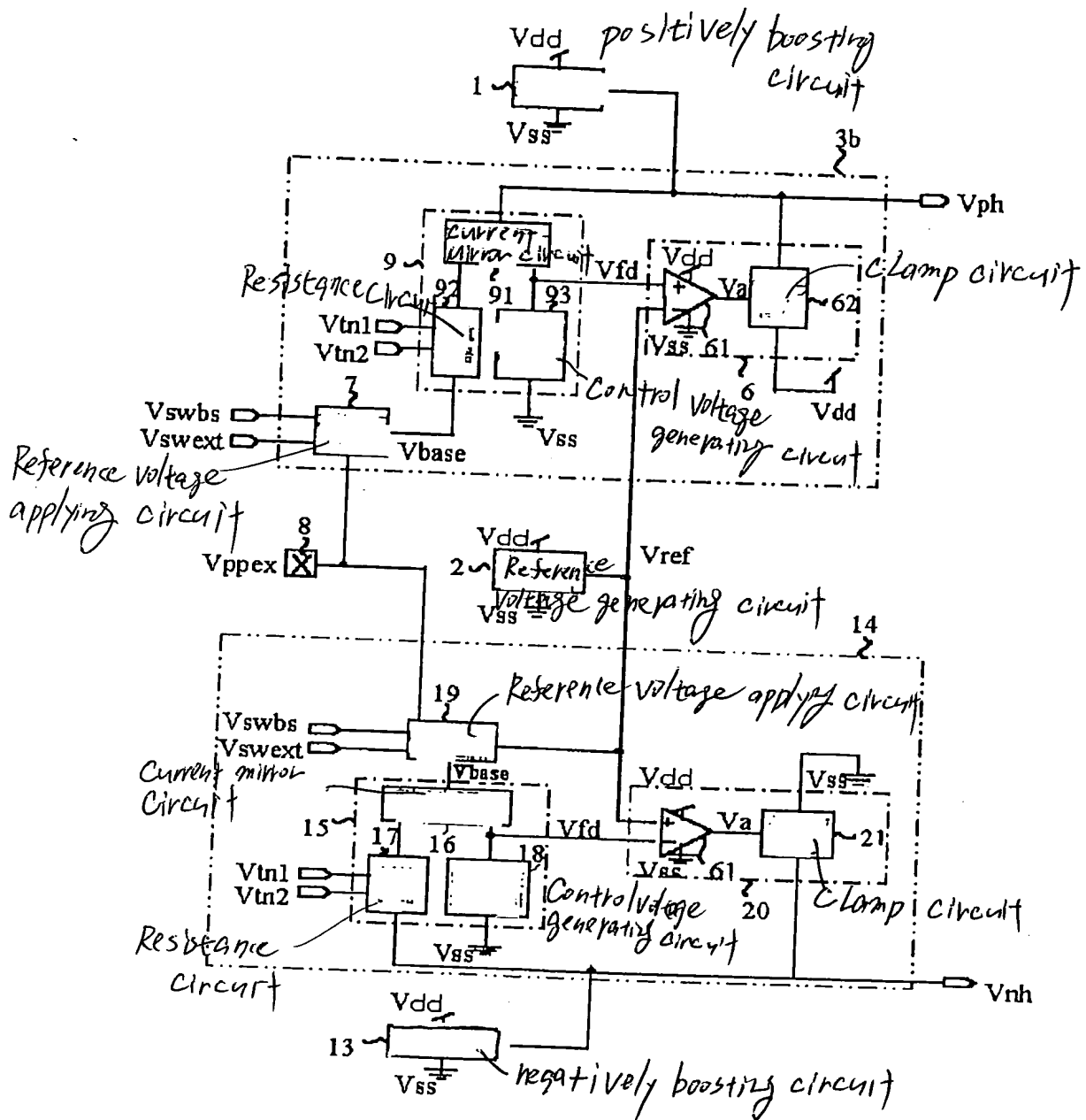


Fig-29



add positively boosting circuit



Fig-31

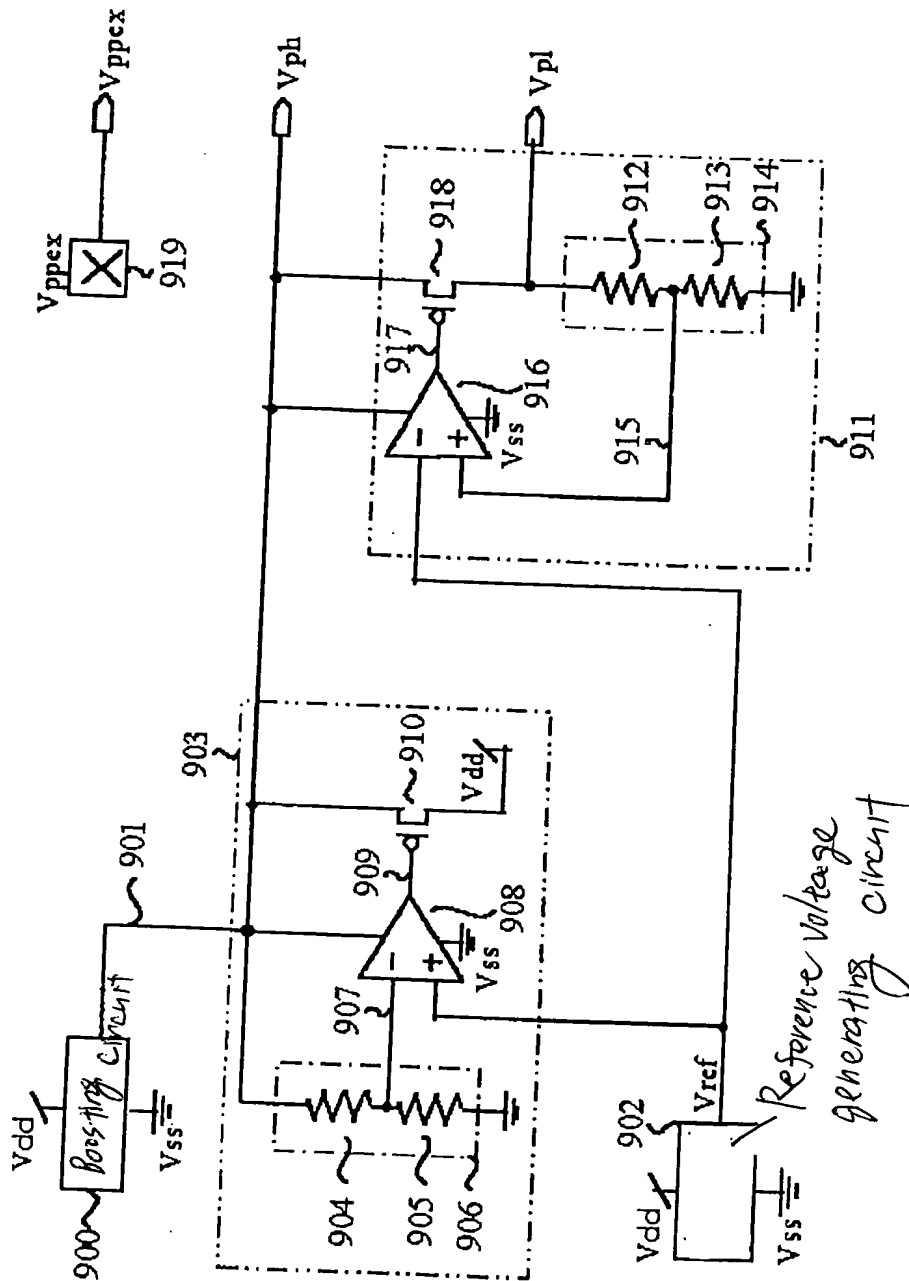


Fig. 32

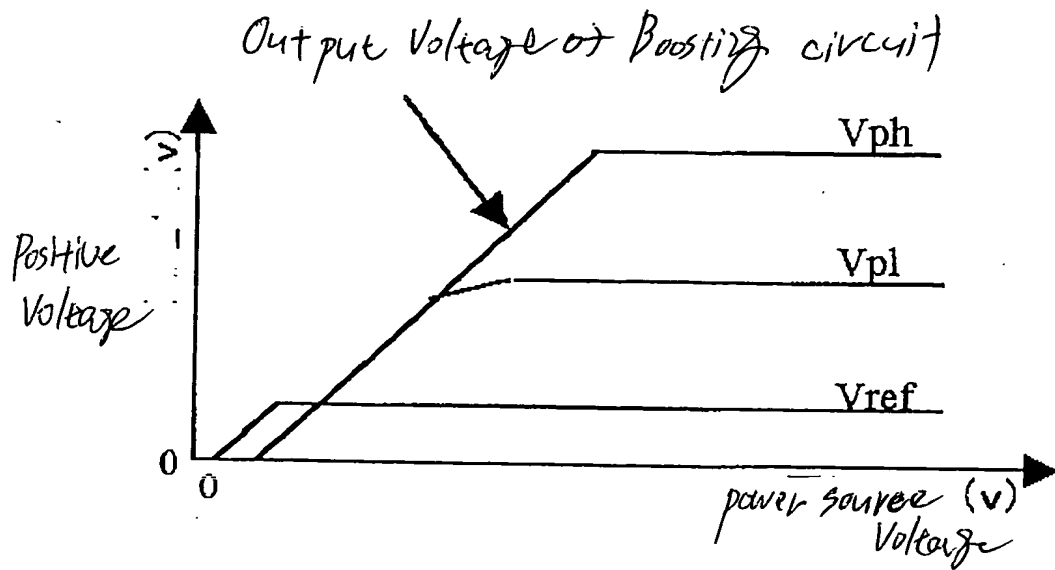


Fig. 33

